Hardware/Software Codesign Lab 3

Student Name:

Student ID:

1. Follow the Lab 3 manual finish Lab 3.
2. Copy and paste the following information to the end of this document and submit this document:
3. Block diagram for your hardware platform.
4. peripheral memory map
5. system.hdf: highlight information for the custom IP added and BRAM and BRAM controller.
6. Pin assignment for the four LEDs
7. Answer the following question:
8. Which register in the custom IP is used to control the leds? Can we use a different one? Show the modified code to use a different register to control the leds.
9. Can we move the instantiation of lab3\_user\_logic from led\_ip\_v1\_0\_S\_AXI.v to led\_ip\_v1\_0, why or why not?